

Modelling and Measurements of 10 Gb/s RZ Integrated Optical Modulators

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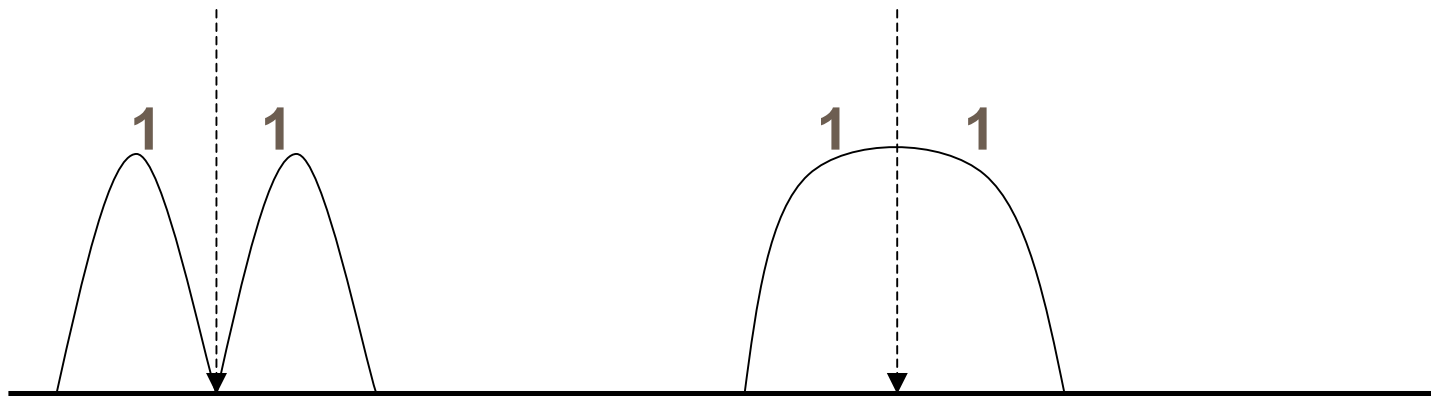
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Introduction

What is a RZ Integrated Optical Modulator ?

- **RZ**: Return to Zero
- **NRZ**: Not Return to Zero



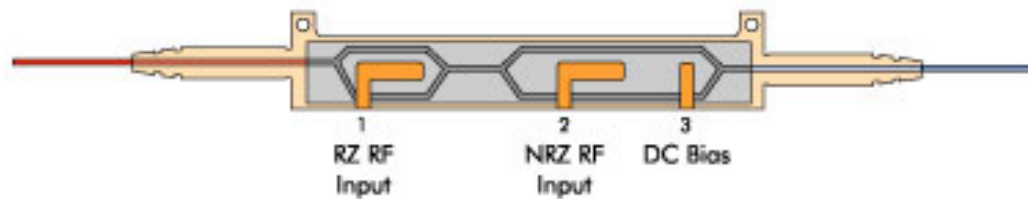
Modulator: LiNbO₃ , X-cut, CPW



Introduction

A typical integrated RZ and NRZ modulator

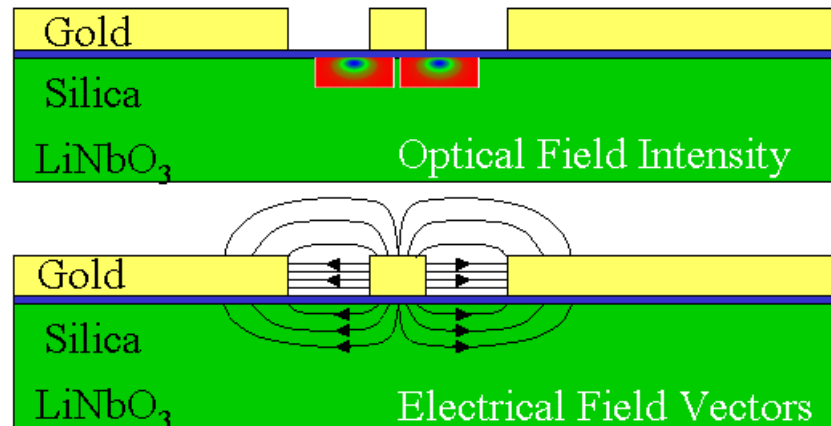
- Integrated RZ-pulse & NRZ within same package
- Single part for C & L-band operation
- Packaging technology
 - based on JDSU's existing hermetic product
 - qualified true-hermetic to Telcordia-468
- Chip design based on JDSU's X-cut 10Gb/s technology building blocks.



Introduction

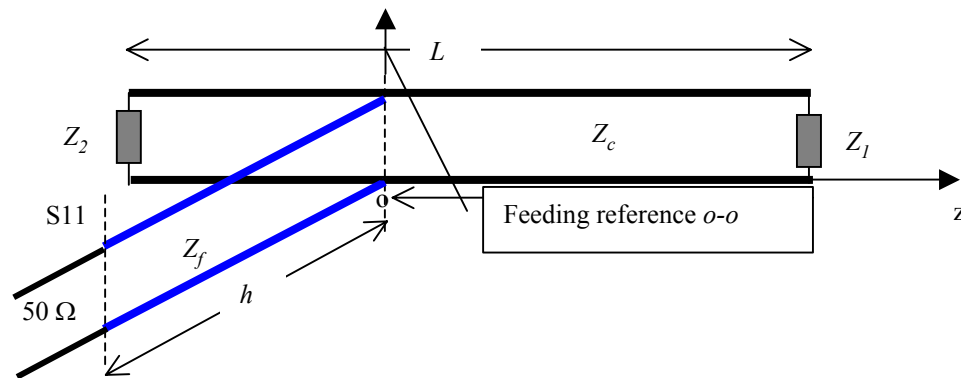
Advantages of RZ modulator

- Longer transmission distance
- Smaller in size
- Suitable for high data-rate applications
- Easy for integration



Theoretical Model

- Based on transmission line theory



- It can be used to find:
 - The voltage/current distribution, input impedance
- **Objective:**
 - to make it resonate at the desired operation frequency



Theoretical Model

– Phase shift:

$$\Delta\phi(\varphi_0, f) = \text{Re} \left[\int_0^L \frac{\pi}{\lambda} r_{33} n_o^3 \frac{V(z)}{g} \Gamma e^{j(\omega z / v_0 + \varphi(z) + \varphi_0)} dz \right]$$

λ is the optical wavelength,

r_{33} the electro-optical coefficient ($30.8 \times 10^{-12} \text{m/V}$ for LiNbO_3),

n_o the optical extraordinary index, g the gap of the CPW,

v_0 the speed of optical wave,

φ_0 the reference phase of the standing wave,

ω the angular frequency of microwave,

Γ is the overlap integral factor, between 0 – 1



Theoretical model

Calculation of S_{11} , S_{21} , and V_{π}

- S_{11} : the electric reflection coefficient at the microwave input, *i.e.*, V_{in}/V_{out}
 - the smaller, the better at the operation frequency
- S_{21} : the optical transmission coefficient, or optical frequency response

$$S_{21} = OR(f) = 20 \log_{10} \left(\left| \frac{\Delta\phi(f)}{\Delta\phi(0)} \right| \right)$$

- V_{π} : the half-wave driving voltage

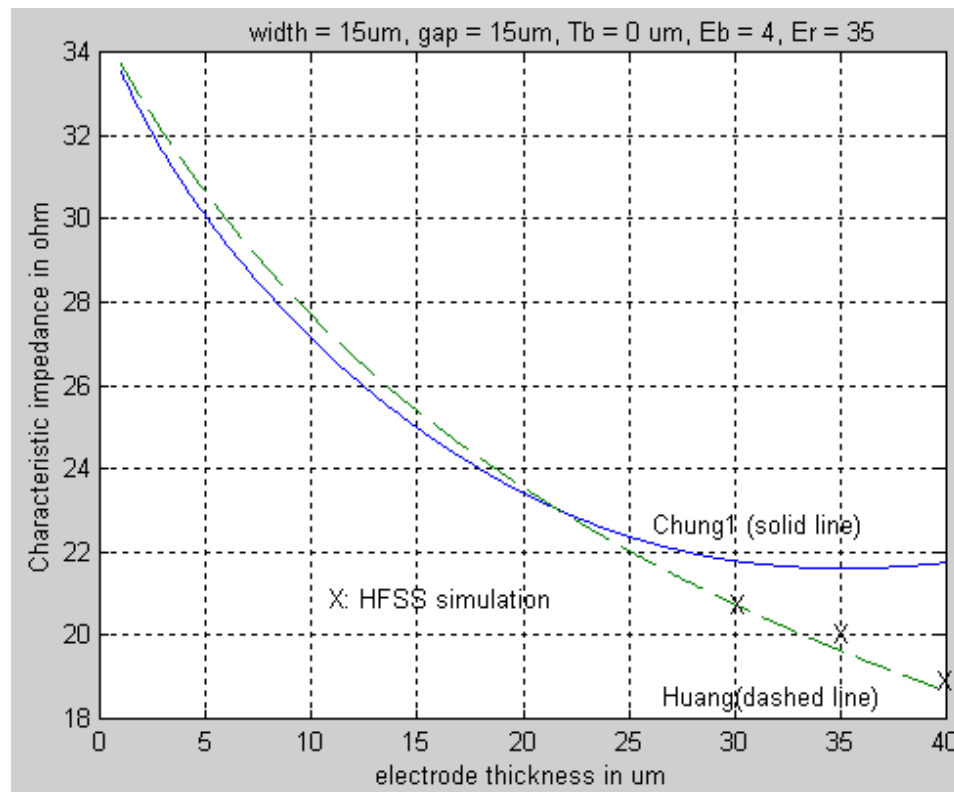
$$V_{\pi} = \frac{\pi V_0}{2\Delta\phi(f)}$$



Theoretical model

Calculation of Z_c

- Formula for thin CPW available, but not for thick CPW



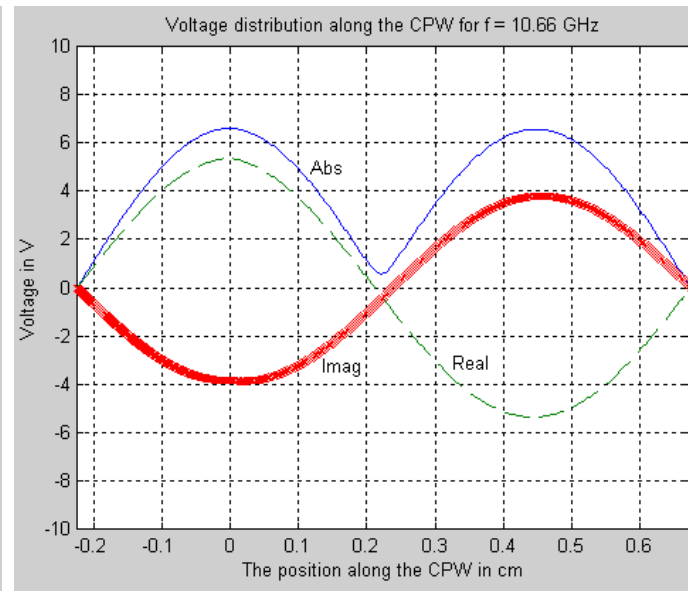
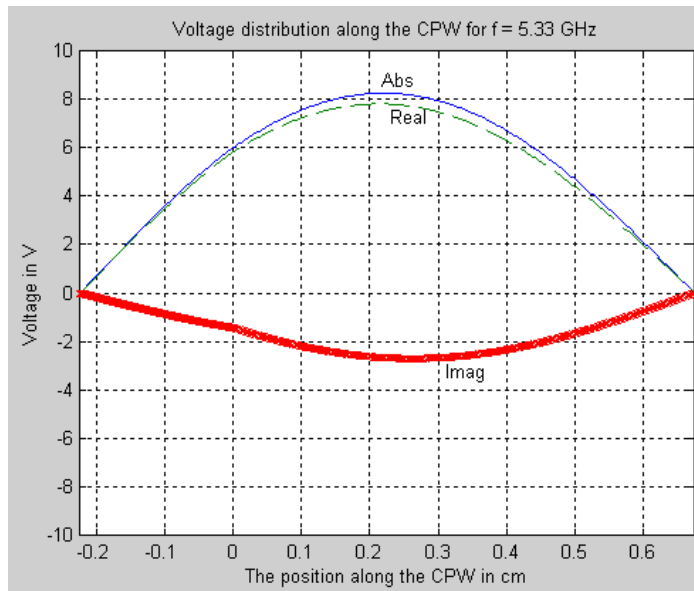
Measurements

- S11 were measured using Network Analyser
- S21 were measured using Electro-optic Network Analyser
- V_{π} : normalised to 50Ω , were obtained by measuring the extinction ratio at the DC and RF



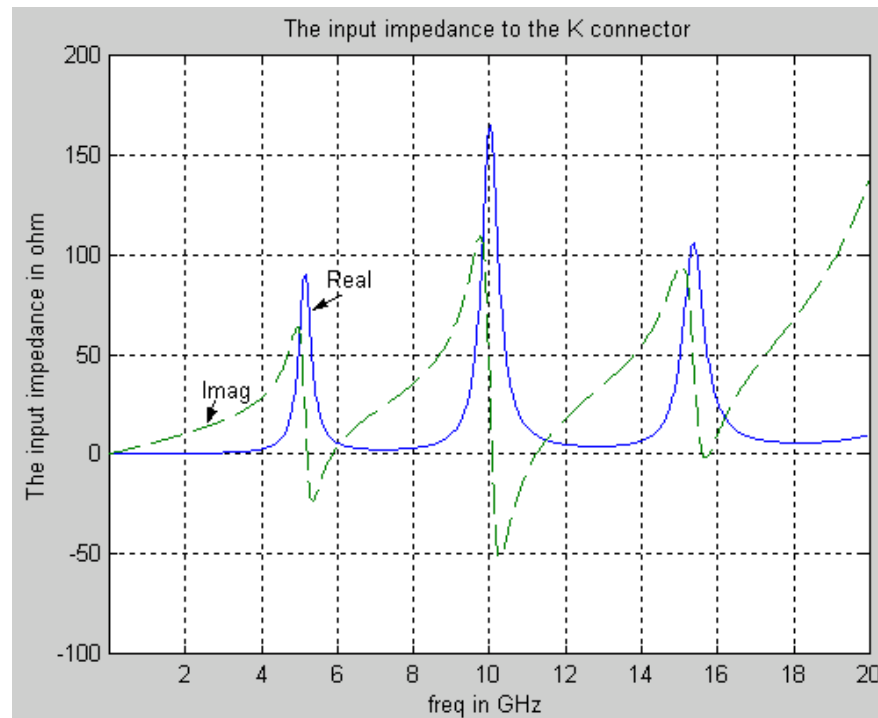
Results and Discussion

- Voltage distribution along the chip (short-end):



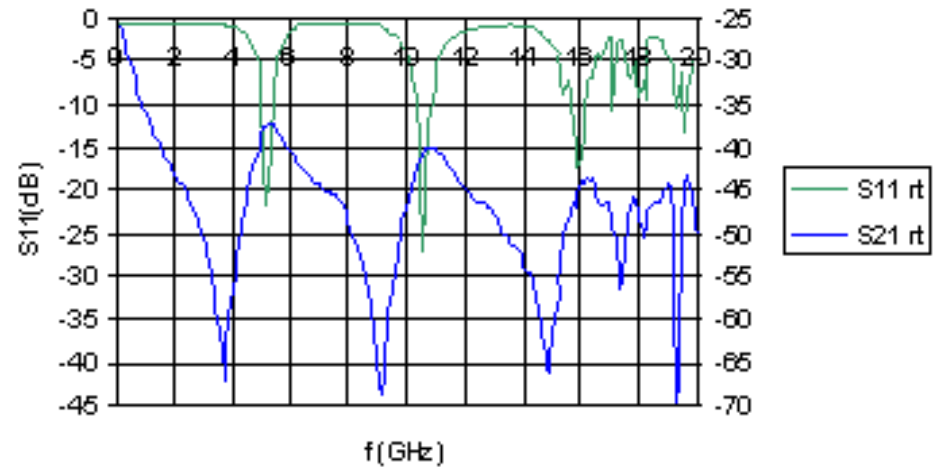
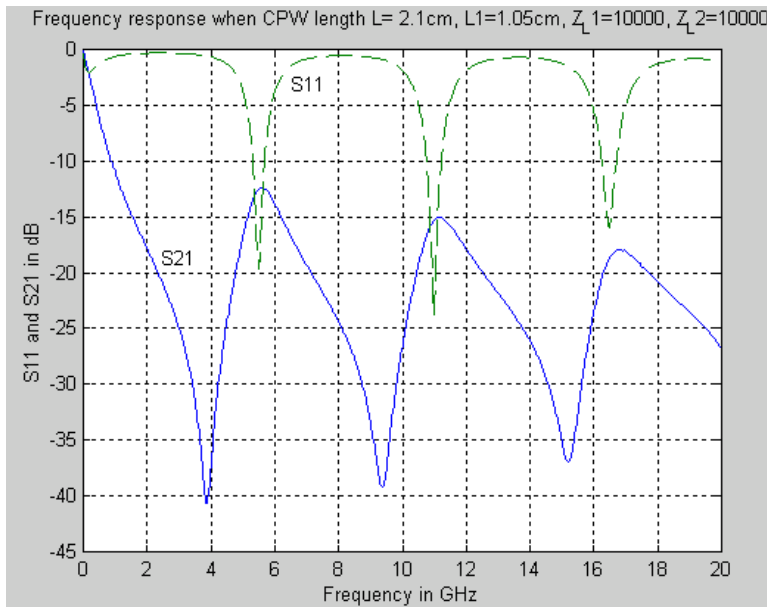
Results and Discussion

- Impedance vs. Frequency



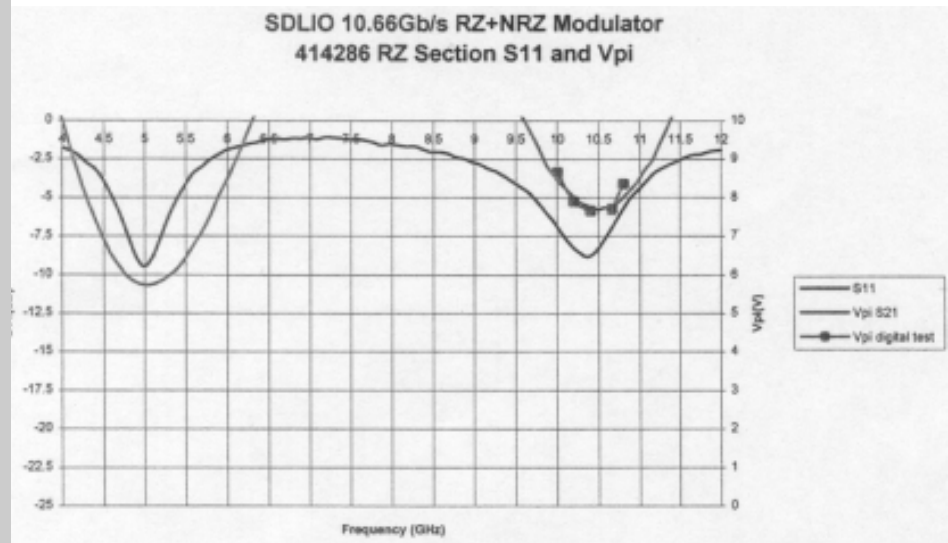
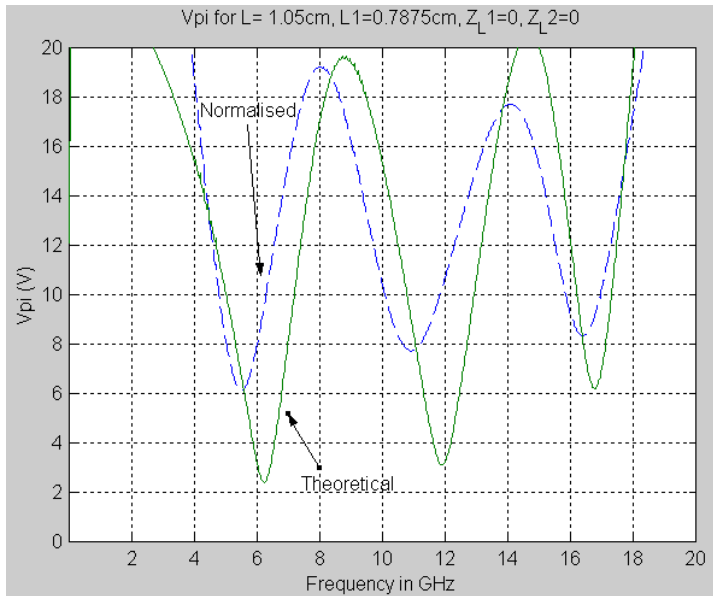
Results and Discussion

- Simulated and Measured S11 (<-10 dB) and S21



Results and Discussion

- Simulated and measured $V_{\pi} < 8V$



Conclusions

- A theoretical model based on transmission line theory has been developed.
- It has been used to obtain important parameters, such as S_{11} , S_{21} , and V_{π}
- The predicted and measured results are in good agreement:
 - Typical $S_{11} < -10$ dB at the operation frequency
 - Typical $V_{\pi} < 8$ V

